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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/766,907	01/30/2004	Sung-Hee Hwang	1793.1155	2240
	7590 02/12/2007 VEN & BUI, LLP	EXAMINER		
1400 EYE STREET, NW SUITE 300 WASHINGTON, DC 20005			ALPHONSE, FRITZ	
			ART UNIT	PAPER NUMBER
			2133	
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SHORTENED STATUTOR	Y PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE	
3 MONTHS		02/12/2007	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

	Application No.	Applicant(s)				
	10/766,907	HWANG ET AL.				
Office Action Summary	Examiner	Art Unit				
·	Fritz Alphonse	2133				
The MAILING DATE of this communication app	ears on the cover sheet with the	correspondence address				
Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATIO 36(a). In no event, however, may a reply be ti vill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONI	N. mely filed n the mailing date of this communication. ED (35 U.S.C. § 133).				
Status						
1)⊠ Responsive to communication(s) filed on 13 No.	ovember 2006	·				
<u> </u>	action is non-final.	•				
<u>,=</u>	,—					
closed in accordance with the practice under E	•					
Disposition of Claims						
4)⊠ Claim(s) <u>1-18</u> is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1,2,5,6 and 8-17</u> is/are rejected.						
7) Claim(s) <u>3,4,7 and 18</u> is/are objected to.						
8) Claim(s) are subject to restriction and/or	r election requirement.	· · · · ·				
Application Papers						
	•					
9) The specification is objected to by the Examine		d to booth a Francisco				
10)☑ The drawing(s) filed on <u>30 January 2004</u> is/are: a)☑ accepted or b)☐ objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correct		· ·				
11) The oath or declaration is objected to by the Ex						
Priority under 35 U.S.C. § 119						
	priority under 35 H S C & 110/a	a)-(d) or (f)				
12)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)⊠ All b)□ Some * c)□ None of:						
1 ⊠ Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No						
3. Copies of the certified copies of the prior						
application from the International Bureau		Ğ				
* See the attached detailed Office action for a list	of the certified copies not receive	ed.				
Attachment(s)						
1) X Notice of References Cited (PTO-892)	4) Interview Summary	(PTO-413)				
2) D Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail D	ate				
B) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	5) Notice of Informal F	Patent Application (PTO-152)				

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DETAILED ACTION

0.1 This Office Action is in response to the amendment filed on 11/13/2006. Claims 1-18 are pending.

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-2, 5-6, 8-10, 11-17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Inoue (U.S. Pat. No. 5,696,774) in view of Arai (U.S. Pat. No. 5,757,824) and further in view of Applicant admitted Prior Art (APA).

As to claim 1, Inoue (figs. 13) teaches an apparatus for generating an error flag, the apparatus including a BIS (Burst Indicator Subcode) error flag memory which stores a BIS error flag for the at least one data block (fig. 29; col. 31, lines 64 through col. 32 line 20).

Inoue does not explicitly disclose an error flag generator, which generates an error flag indicating an error existence/absence for ECC (Error-Correction Coding) data with reference to the frame-sync error information stored in the frame-sync error memory and the BIS error flag stored in the BIS error flag memory.

However, in the same field of endeavor, Arai (fig. 17) shows a code error correction apparatus including an error flag generator (57), which generates an error flag indicating an error existence/absence for ECC (Error-Correction Coding) data (col. 12, lines 66 through col. 13 line 6).

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Therefore, it would have been obvious to a person of ordinary skill in the art, at the time of the invention to incorporate the error flag generator (57) as taught by Arai in the digital signal recording device, as disclosed by Inoue. Doing so would provide a configuration of an error correction code and a decoding apparatus, which is highly capable of error correction without great quality deterioration of a reproduction signal even when there are many code errors (col. 2, lines 16-20).

In addition, as to claim 1, Inoue does not explicitly disclose a frame-sync error memory which stores frame-sync error information for at least one data block. However, the limitation is obvious and very well known in the art, as evidenced by APA (see specification [0006-0007], fig. 1).

As to claims 2, 6 and 9, Inoue does not explicitly disclose the frame-sync error memory stores frame-sync error information corresponding to at least two data blocks. However, the limitation is disclosed by APA (see specification [0006-0007], fig. 1).

As to claim 10, Inoue discloses an apparatus, wherein the BIS error flag memory stores BIS error flag corresponding to at least two data blocks (col. 40, lines 15-35).

As to claim 5, method claim 5 corresponds to apparatus claim 1; therefore, it is analyzed as previously discussed in claim 1 above.

As to claim 8, the claim differs from claim 1 by the additional limitation "a frame-sync detector, outputting frame-sync error information indicating an existence/absence of an error for frame sync-data of frames forming data blocks." However, the limitation is clearly disclosed by APA (see specification [0006-0007], fig. 1).

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As to claims 11-17, the claims have substantially the limitations of claims 2, 6 and 9; therefore, they are analyzed as previously discussed in claims 2, 6 and 9 above.

Allowable Subject Matter

3. Claims 3-4, 7 and 18 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Response to Arguments

4. Applicant's arguments with respect to claims 1-18 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

- 5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. See PTO-892
- 6. Any response to this action should be mailed to:

Commissioner of Patents and Trademarks, Washington, D.C. 20231

or faxed to: (703) 872-9306 for all formal communications.

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Fourth Floor (Receptionist).

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Fritz Alphonse, whose telephone number is (571) 272-3813. The examiner can normally be reached on M-F, 8:30-6:00, Alt. Mondays off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert De Cady, can be reached at (571) 272-3819.

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Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (571) 272-3824.

Information regarding the status of an application may also be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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February 3, 2007

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100